

FIG. 1 (PRIOR ART)

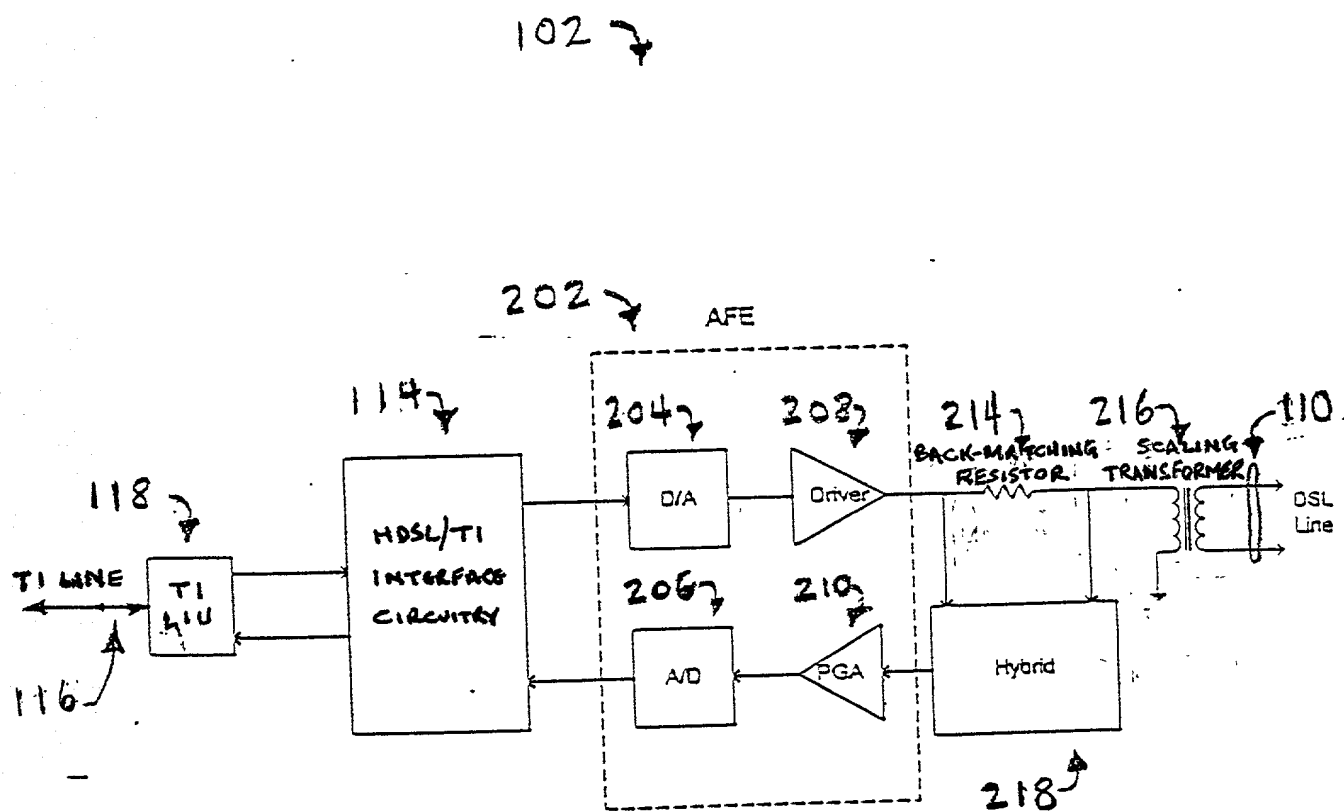


FIG. 2 (PRIOR ART)

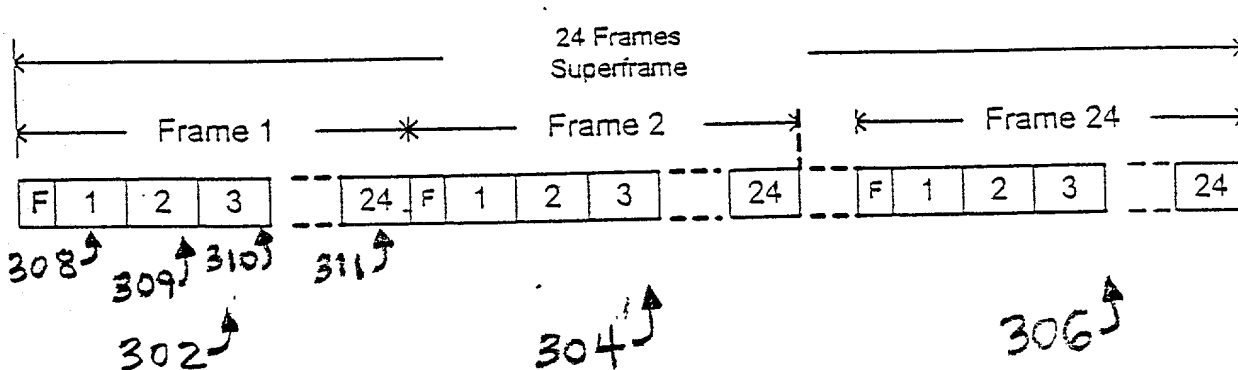


FIG. 3 (PRIOR ART)

400

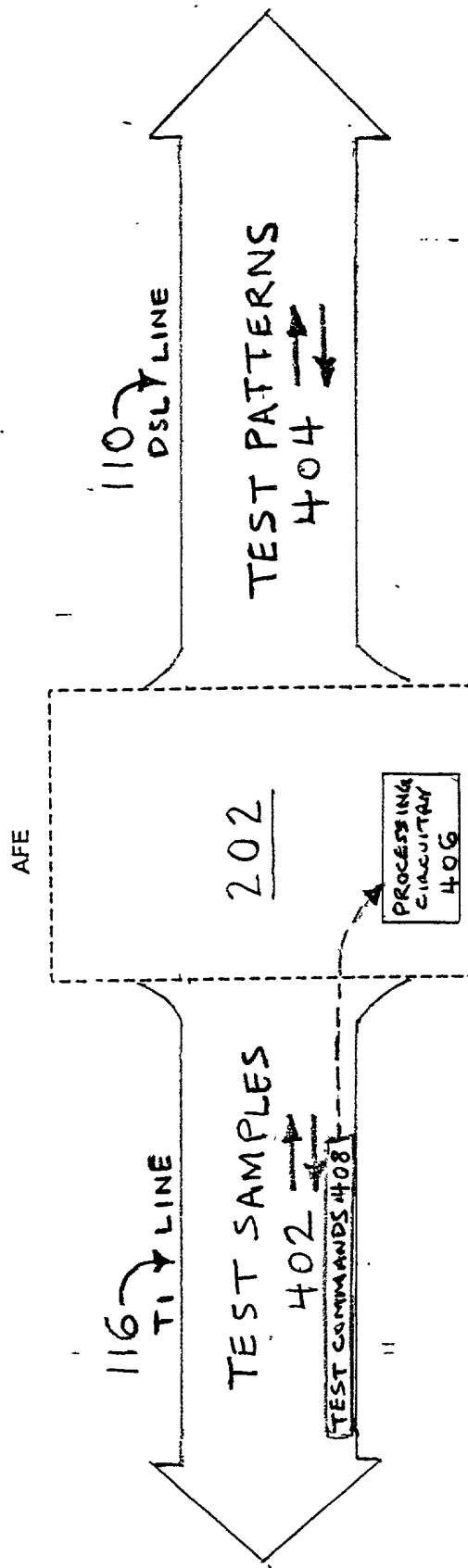


FIG. 4

500 ↘

502 ↘

Test Frame 0

12 Bits

12 Bits



522 ↗

506 ↗

508 ↗

504 ↗
⋮

510 ↗

512 ↗

Test Frame N

12 Bits

12 Bits



524 ↗

514 ↗

516 ↗

518 ↗

520 ↗

FIG. 5

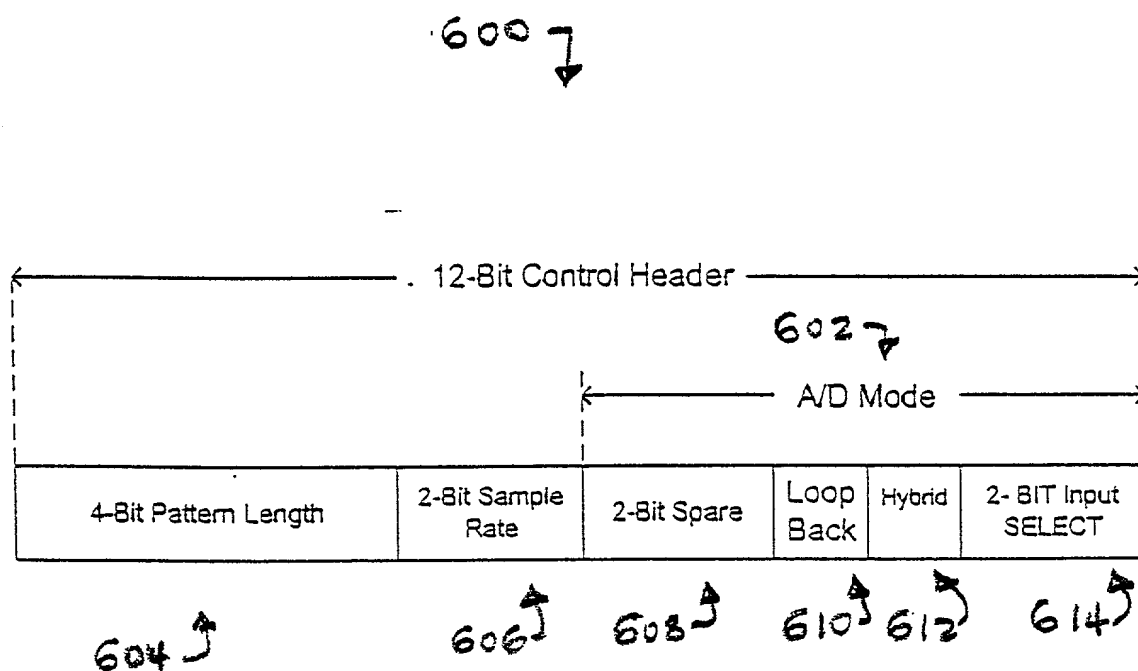


FIG. 6

700 ↘

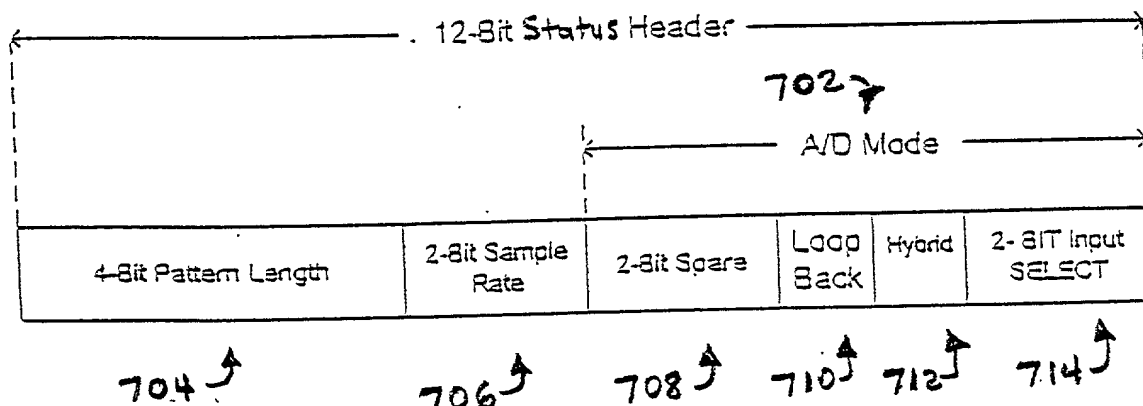


FIG. 7

802

800

AFE

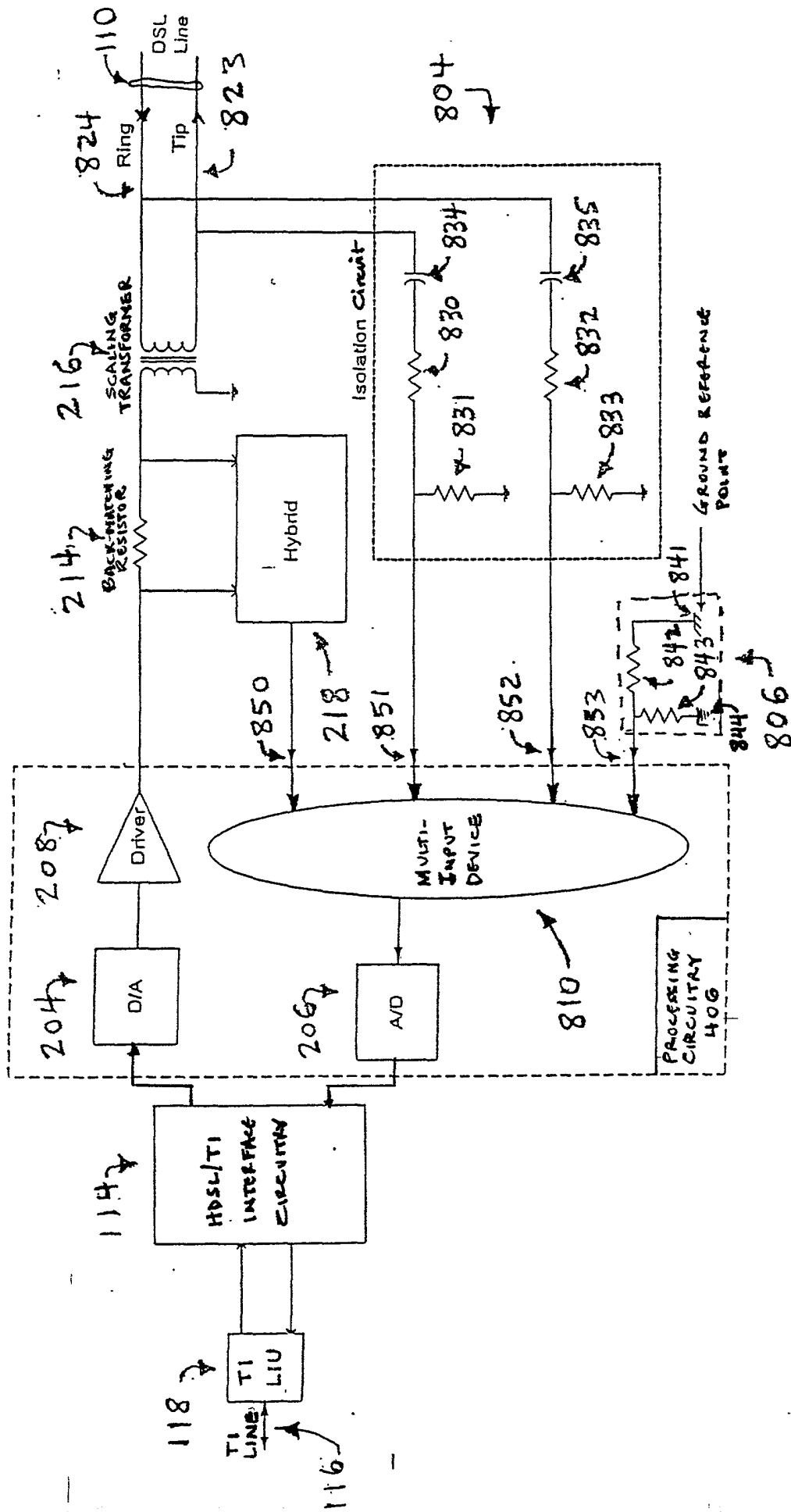


FIG. 8

FIG. 9 is a flowchart illustrating a process for testing a DSL line via an AFE. The process starts at 902, then transmits test samples to the AFE at 904. The AFE interprets the test samples as digitized values at 906. The AFE generates test patterns from the digitized values at 908. The DSL line is tested via the AFE using the test patterns at 910. The process ends at 912.

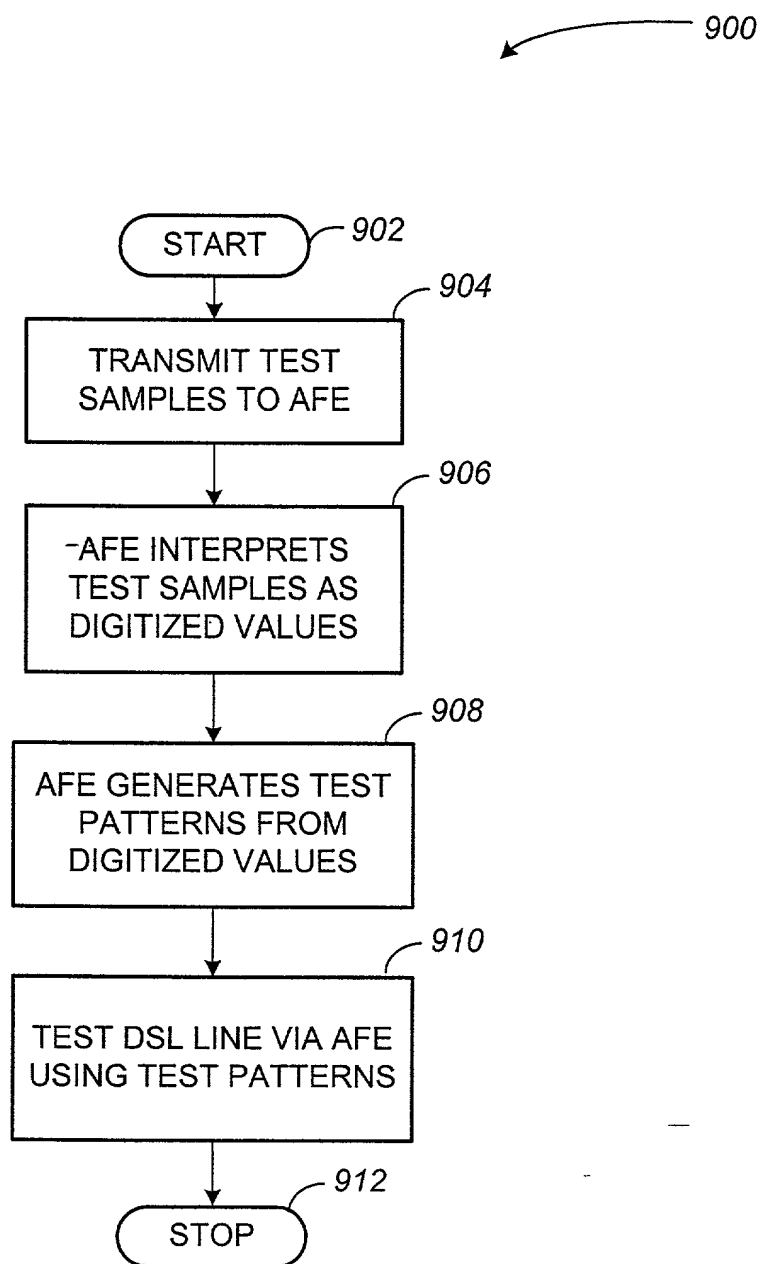


FIG. 9

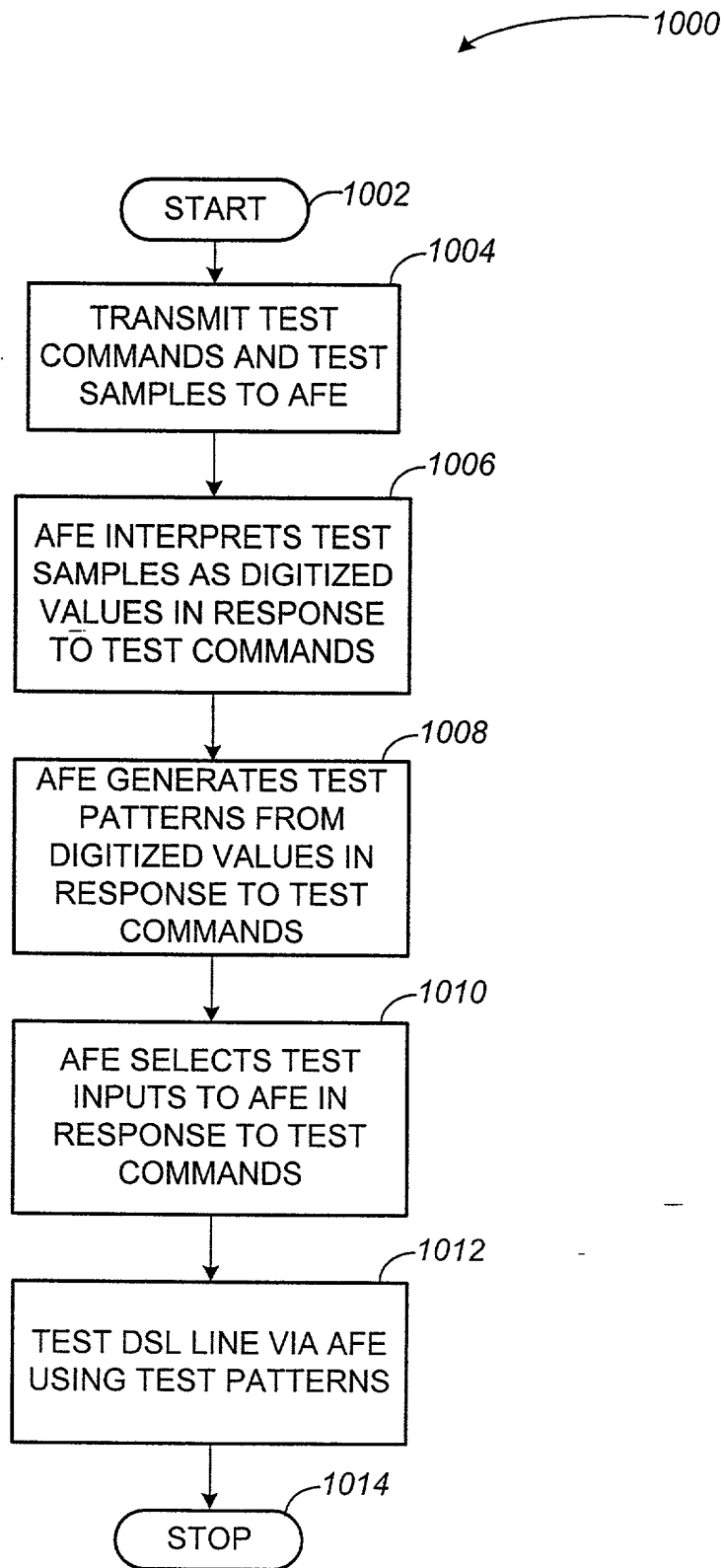


FIG. 10